IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Peter C. Damron

SERIAL NO.:   09/629,085

FILING DATE: July 31, 2000

TITLE: Method and System for Translation Lookaside Buffer Coherence in Multiprocessor Systems

EXAMINER: Reginald Glenwood Bragdon   (Tel. No.: (703) 305-3823)

(Fax No.: (703) 872-9306)

ART UNIT: 2188

OFFICE OF INITIAL PATENT EXAMINATIONS
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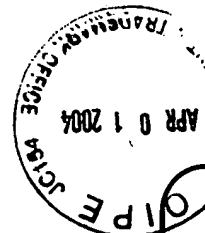P.O. Box 1450 Alexandria, VA 22313-1450

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REQUEST TO CORRECT FILING RECEIPT

It is respectfully submitted that the Filing Receipt for the above-identified patent

application has an error.

The title is incorrect. The correct title is "Method and System for Translation

Lookaside Buffer Coherence in Multiprocessor Systems" and not "Method and System

for Translation Lookaside Buffer Cohernce in Multiprocessor Systems" as indicated on

the Filing Receipt. Please amend the Filing Receipt and appropriate records to reflect the

correct title. Enclosed is a copy of the first page of the specification filed on July 31,

2000, for the above referenced patent application. Please note that it correctly lists the

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	09/629,085
Filing Date	July 31, 2000
First Named Inventor	Peter C. Damron
Art Unit	2188
Examiner Name	Reginald G. Bragdon

Total Number of Pages in This Submission	7	Attorney Docket Number	SUN-P4935

ENCLOSURES *(check all that apply)* .

☐ Fee Transmittal Form

☐ Fee Attached

☐ Amendment / Reply

☐ After Final

☐ Affidavits/declaration(s)

☐ Extension of Time Request

☐ Express Abandonment Request

☐ Information Disclosure Statement

☐ Certified Copy of Priority Document(s)

☐ Response to Missing Parts/ Incomplete Application

☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

☐ Drawing(s)

☐ Licensing-related Papers

☐ Petition

☐ Petition to Convert to a Provisional Application

☒ Change of Correspondence Address

☐ Terminal Disclaimer

☐ Request for Refund

☐ CD, Number of CD(s) _____

☐ After Allowance Communication to Group

☐ Appeal Communication to Board of Appeals and Interferences

☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)

☐ Proprietary Information

☐ Status Letter

☒ Other Enclosure(s) *(please identify below)*:

Request to Correct Filing Receipt, copy of 1st page of spec., and copy of PTO Filing Receipt dated 9/7/00

Remarks

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	John P. Schaub, Reg. No. 42,125
Signature	*[signature]*
Date	3/28/04

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.

Typed or printed name	Carol Diez		
Signature	*[signature]*	Date	3/29/84

title of the invention. In addition a copy of the Filing Receipt showing the requested

change is enclosed.

The Commissioner is hereby authorized to charge any additional fees or credit any

overpayment to Deposit Account No. 50-1698.
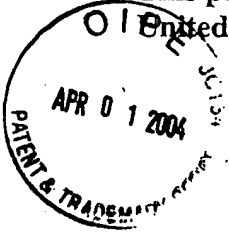
Respectfully submitted,
THELEN REID & PRIEST LLP

Dated: March _28_, 2004

John P. Schaub
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This patent application is submitted in the name of inventor Peter C. Damron, a citizen of United States, assignor to Sun Microsystems, Inc., a Delaware Corporation.

SPECIFICATION

TITLE OF THE INVENTION

METHOD AND SYSTEM FOR TRANSLATION LOOKASIDE BUFFER COHERENCE IN MULTI-PROCESSOR SYSTEMS

BACKGROUND OF THE INVENTION

Field of the Invention

This invention relates to an apparatus, method and system for translation lookaside buffer ("TLB") coherence in computer systems; more particularly, this invention relates to an apparatus, method and system for TLB coherence in multi-processor computer systems.

The Related Art

Virtual memory systems allow the addressing of large amounts of memory as though all of that memory were the main memory of the computer system even though actual main memory may consist of a substantially lessor amount of storage space. Virtual memory systems accomplish this by providing memory management units which translate virtual memory addresses into physical memory addresses. The physical addresses may be stored in the main computer memory, in cache memory systems, or otherwise.

If the physical address is kept in the main computer memory, the main computer memory uses lookup tables to locate the physical address. The computer compares the virtual address to the values stored in the tables to determine the physical address. There are often several levels of tables, and the comparison takes a long time to locate. To overcome this delay, the physical address may be stored in cache memories. Cache memories are fast components to store recently

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/629,085	07/31/2000	2751	768	SUN-P4935	3	18	4

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OC000000005380442

Date Mailed: 09/07/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)

Peter C. Damron, Fremont, CA ;

Continuing Data as Claimed by Applicant

Reviewed By: _____ Date: 9/13/00
No Action Required: _____ X
Action Required: _____

Foreign Applications

If Required, Foreign Filing License Granted 09/06/2000

Title

Method and system for translation lookaside buffer ~~cohernce~~ in multi-processor systems

coherence

Preliminary Class

711

Data entry by : BRUNSON, MONIQUE   Team : OIPE   **Date: 09/07/2000**